# Plastic Medium Power Silicon NPN Transistor

 $\ldots$  designed for use as audio amplifiers and drivers utilizing complementary or quasi complementary circuits.

- DC Current Gain  $h_{FE} = 40$  (Min) @  $I_{C} = 0.15$  Adc
- BD 135, 137, 139 are complementary with BD 136, 138, 140

BD135 BD137 BD139

1.5 AMPERE
POWER TRANSISTORS
NPN SILICON
45, 60, 80 VOLTS
10 WATTS



# **MAXIMUM RATINGS**

Rating	Symbol	Туре	Value	Unit
Collector–Emitter Voltage	VCEO	BD 135 BD 137 BD 139	45 60 80	Vdc
Collector-Base Voltage	VCBO	BD 135 BD 137 BD 139	45 60 100	Vdc
Emitter–Base Voltage	VEBO		5	Vdc
Collector Current	IC		1.5	Adc
Base Current	lΒ		0.5	Adc
Total Device Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>		1.25 10	Watts mW/°C
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	PD		12.5 100	Watt mW/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +150	°C

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	θJC	10	°C/W
Thermal Resistance, Junction to Ambient	$\theta$ JA	100	°C/W

# BD135 BD137 BD139

# **ELECTRICAL CHARACTERISTICS** ( $T_C = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Туре	Min	Max	Unlt
Collector–Emitter Sustaining Voltage* (IC = 0.03 Adc, IB = 0)	BV <sub>CEO*</sub>	BD 135 BD 137 BD 139	45 60 80	  -  -	Vdc
Collector Cutoff Current $(V_{CB} = 30 \text{ Vdc}, I_E = 0)$ $(V_{CB} = 30 \text{ Vdc}, I_E = 0, T_C = 125^{\circ}\text{C})$	ICBO			0.1 10	μAdc
Emitter Cutoff Current (VBE = 5.0 Vdc, I <sub>C</sub> = 0)	I <sub>EBO</sub>		_	10	μAdc
DC Current Gain ( $I_{C} = 0.005 \text{ A}, V_{CE} = 2 \text{ V}$ ) ( $I_{C} = 0.15 \text{ A}, V_{CE} = 2 \text{ V}$ ) ( $I_{C} = 0.5 \text{ A V}_{CE} = 2 \text{ V}$ )	h <sub>FE</sub> *		25 40 25	 250 	_
Collector–Emitter Saturation Voltage* (I <sub>C</sub> = 0.5 Adc, I <sub>B</sub> = 0.05 Adc)	VCE(sat)*		1	0.5	Vdc
Base–Emitter On Voltage* (I <sub>C</sub> = 0.5 Adc, V <sub>CE</sub> = 2.0 Vdc)	V <sub>BE(on)</sub> *		_	1	Vdc

<sup>\*</sup> Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, Duty Cycle  $\leq$  2.0%.

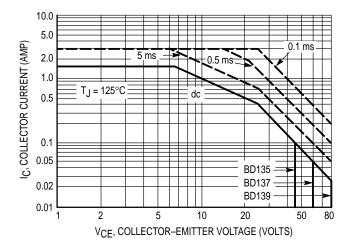
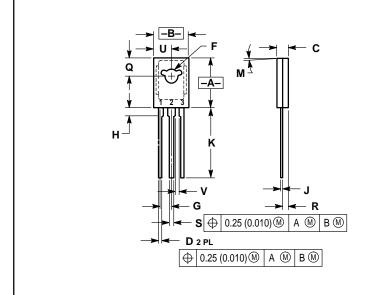


Figure 1. Active-Region Safe Operating Area

# **PACKAGE DIMENSIONS**



- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.425	0.435	10.80	11.04	
В	0.295	0.305	7.50	7.74	
С	0.095	0.105	2.42	2.66	
D	0.020	0.026	0.51	0.66	
F	0.115	0.130	2.93	3.30	
G	0.094	BSC	2.39 BSC		
Н	0.050	0.095	1.27	2.41	
J	0.015	0.025	0.39	0.63	
K	0.575	0.655	14.61	16.63	
M	5°	TYP	5° TYP		
Q	0.148	0.158	3.76	4.01	
R	0.045	0.055	1.15	1.39	
S	0.025	0.035	0.64	88.0	
U	0.145	0.155	3.69	3.93	
٧	0.040		1.02		

STYLE 1:
PIN 1. EMITTER
2. COLLECTOR
3. BASE

CASE 77-08 TO-225AA TYPE **ISSUE V** 

#### BD135 BD137 BD139

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